

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	128048	((circuit adj board) pcb) same (pin terminal connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:44
L2	19233	"439"/\$.ccls. and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:44
L3	9222	(cut\$4 stamp\$4) and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:45
L7	5798	(bent bend\$4 fold\$4) and 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:01
L8	3870	(plate plated plating) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:04
L9	1800	lead and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:00
L10	5323	"174"/\$.ccls. and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:00
L11	2268	(cut\$4 stamp\$4) and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:01

L12	1142	(bent bend\$4 fold\$4) and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:01
L13	811	(plate plated plating) and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:03
L14	481	(174/84c).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 16:03
L15	102342	((circuit adj board) pcb) with (pin terminal connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:03
L16	28639	(cut\$4 stamp\$4) and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:04
L17	17962	(plate plated plating) and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:46
L18	658	(174/94r).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 17:09
L21	73927	((circuit adj board) pcb) near5 (pin terminal connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:45
L22	14815	"439"/\$.ccls. and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:45

L23	17863	((circuit adj board) pcb) near (pin terminal connector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:51
L24	5557	"439"/\$.ccls. and 23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:45
L25	2791	(cut\$4 stamp\$4) and 24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:45
L26	1786	(plate plated plating) and 25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:46
L27	1089	((439/74) or (439/75)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/04 17:13
L34	23173	((circuit adj board) pcb) with (pin terminal connector) with (hole via)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:52
L35	5978	"439"/\$.ccls. and 34	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:52
L36	1899	((circuit adj board) pcb) with (pin terminal connector) with (hole via) with first with second	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:52
L37	769	"439"/\$.ccls. and 36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:52